

Patent Assignment Abstract of Title

Total Assignments: 1

Application #: 10054687 **Filing Dt:** 01/22/2002

Patent #: NONE

Issue Dt:

PCT #: NONE

Publication #: US20030139927 **Pub Dt:** 07/24/2003

Inventors: Thaddeus J. Gabara, Inkyu Lee, Maria Luisa Lopez-Vallejo, Syed Mujtaba

Title: Block processing in a maximum a posteriori processor for reduced power consumption

Assignment: 1

Reel/Frame: <u>012779 /</u>	Received: 04/16/2002	Recorded: 04/11/2002	Mailed: 06/10/2002	Pages: 11
<u>0656</u>				

Conveyance: ASSIGNMENT OF ASSIGNEES INTEREST (SEE DOCUMENT FOR DETAILS).

Assignors: GABARA, THADDEUS J.

Exec Dt: 01/23/2002

LEE, INKYU

Exec Dt: 01/22/2002

LOPEZ-VALLEJO, MARIA LUISA

Exec Dt: 03/20/2002

MUJTABA, SYED A.

Exec Dt: 01/30/2002

Assignee: AGERE SYSTEMS INC.

555 UNION BOULEVARD

ALLEGTON, PENNSYLVANIA 18109

Correspondent: MENDELSOHN & ASSOCIATES, P.C.

STEVE MENDELSOHN

1515 MARKET STREET

SUITE 715

PHILADELPHIA, PA 19102

Search Results as of: 4/19/2006 1:50:56 P.M.

If you have any comments or questions concerning the data displayed, contact OPR / Assignments at 571-272-3350
Web interface last modified: Mar 14, 2006 v.1.9